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TOLER LARSON ABEL

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## PART B - FEE(S) TRANSMITTAL

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34456 7590 01/02/2004

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KATRINA PEATI	(Depositor's name)
KATRINA PEATI	(Signature)
1-16-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/058,854	01/28/2002	Kay Hellig	1458.TT4962	3704

TITLE OF INVENTION: SYSTEM FOR FORMING A SEMICONDUCTOR DEVICE AND METHOD THEREOF INCLUDING IMPLANTING THROUGH A L SHAPED SPACER TO FORM SOURCE AND DRAIN REGIONS

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	04/02/2004
EXAMINER		ART UNIT	CLASS-SUBCLASS		
BREWSTER, WILLIAM M		2823	438-181000		

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Toler, Larson &amp; Abel, LLP

## 3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

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(A) NAME OF ASSIGNEE

Advanced Micro Devices, Inc.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Sunnyvale, CA

Please check the appropriate assignee category or categories (will not be printed on the patent):

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(Authorized Signature)

J. Gustav Larson, Reg. No. 39,263

(Date)

1-16-04

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PTOL-85 (Rev. 11/03) Approved for use through 04/10/2004.

OMB 0651-0033

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**TOLER  
LARSON  
& ABEL**

**FACSIMILE COVER SHEET****DATE:** January 16, 2004**TO:** Examiner William Brewster **FAX NO.:** 703-746-4000  
USPTO GPAU 2823**FROM:** J. Gustav Larson**RE U.S. App. No.:** 10/058,854, filed 01/28/2002**Applicant(s):** Kay Hellig**Atty Dkt No.:** 1458.TT4962**Title:** SYSTEM FOR FORMING A SEMICONDUCTOR DEVICE AND  
METHOD THEREOF INCLUDING IMPLANTING THROUGH...**NO. OF PAGES (including Cover Sheet):** 3**MESSAGE:**

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- ☒ Issue Fee Transmittal (1 pg)
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